

FIG. 1A and FIG. 1B are schematic diagrams of a device in accordance with the present invention.

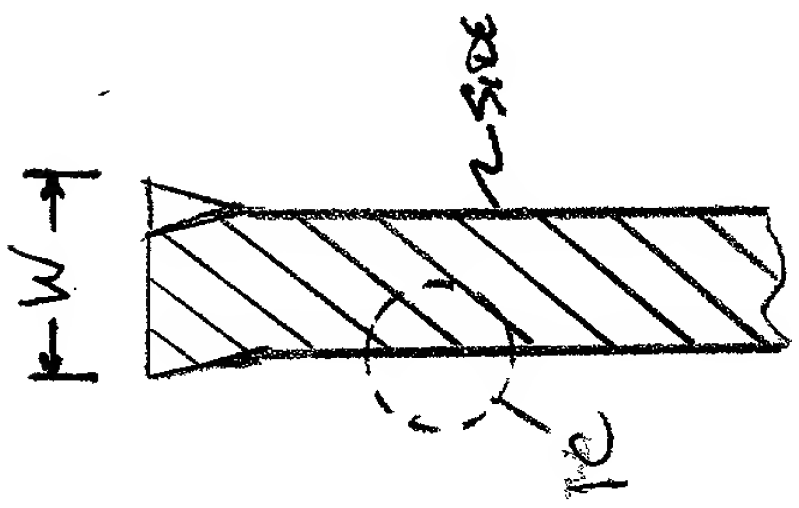


FIG. 1A

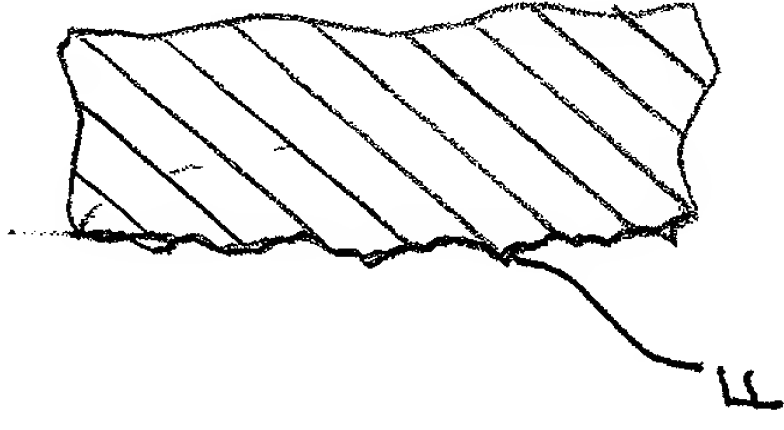


FIG. 1B

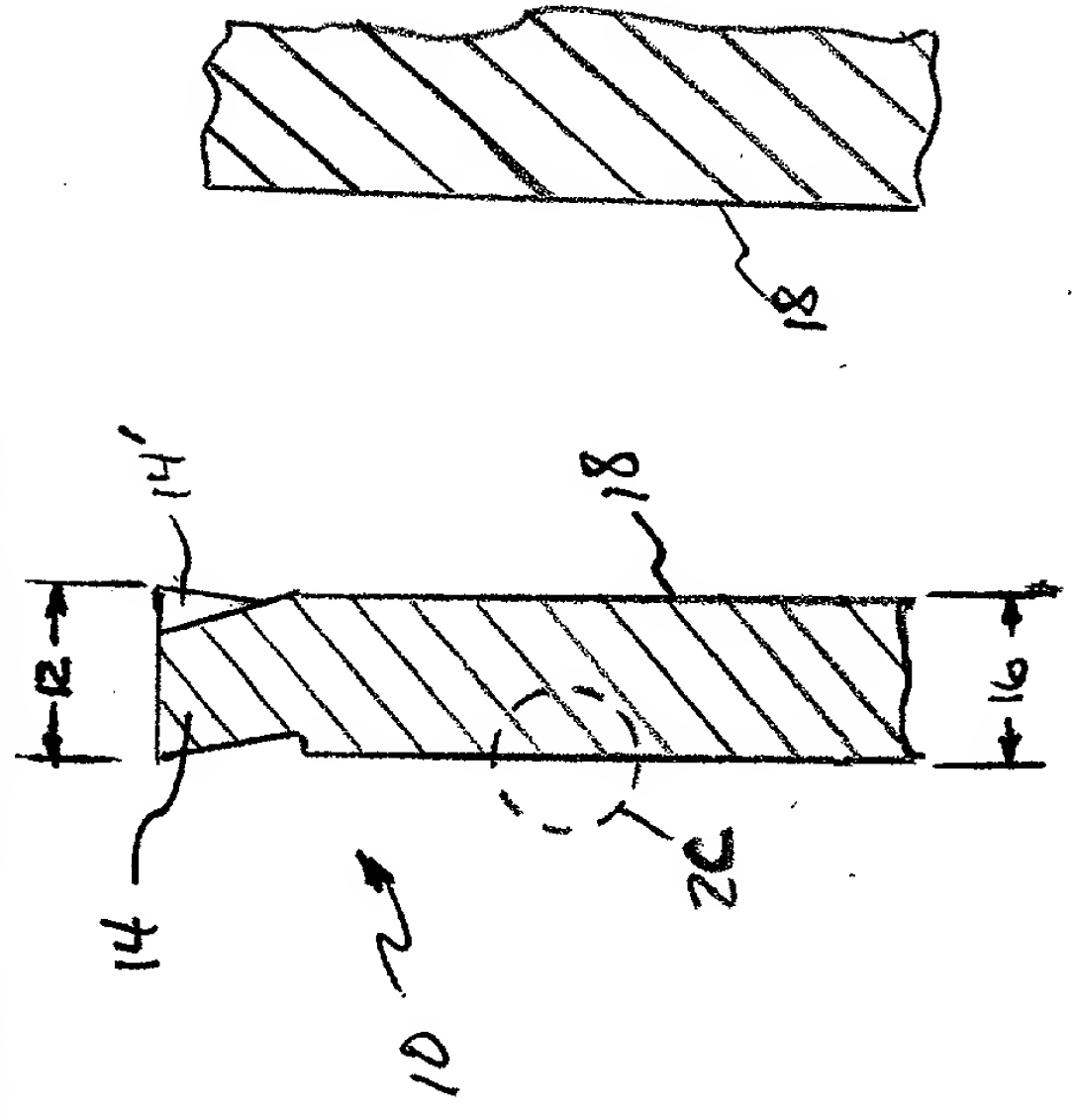


FIG. 2A

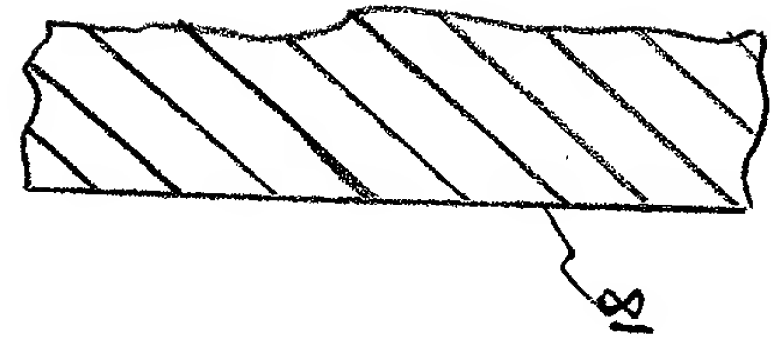


FIG. 2B

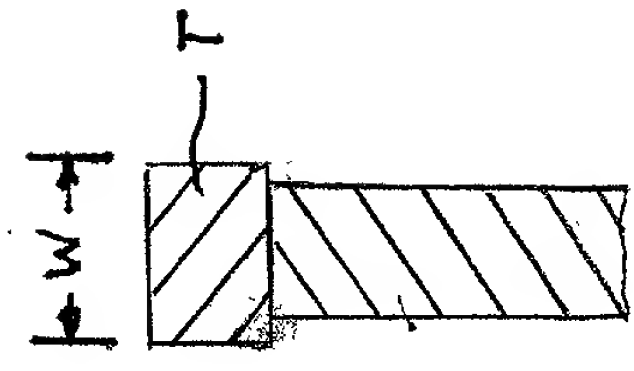


FIG. 2C

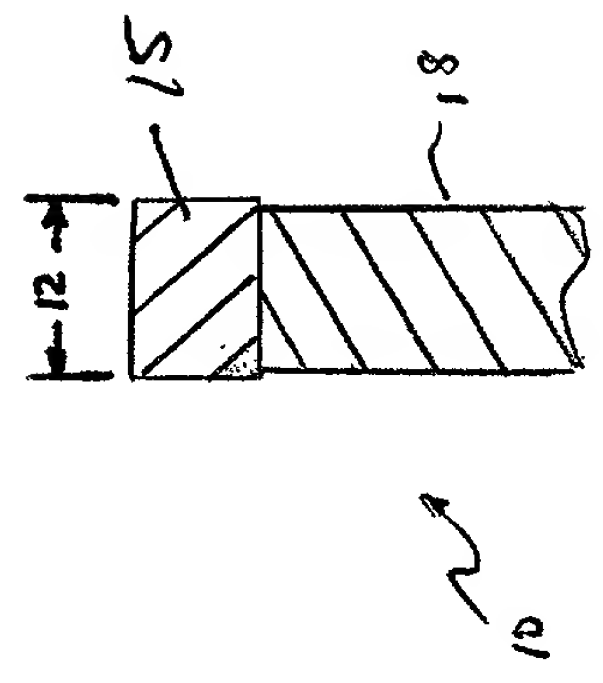


FIG. 2D

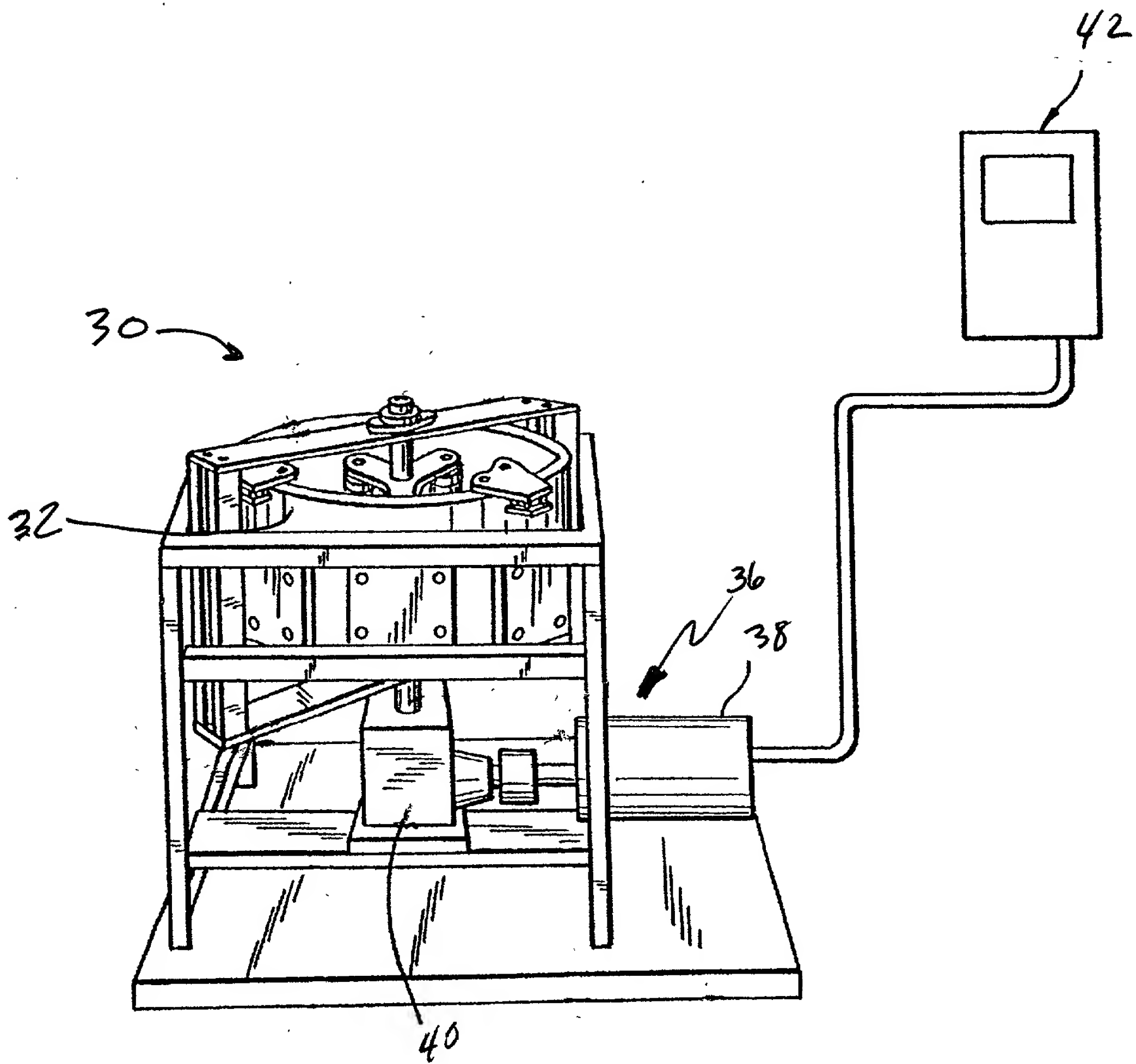


FIG. 3

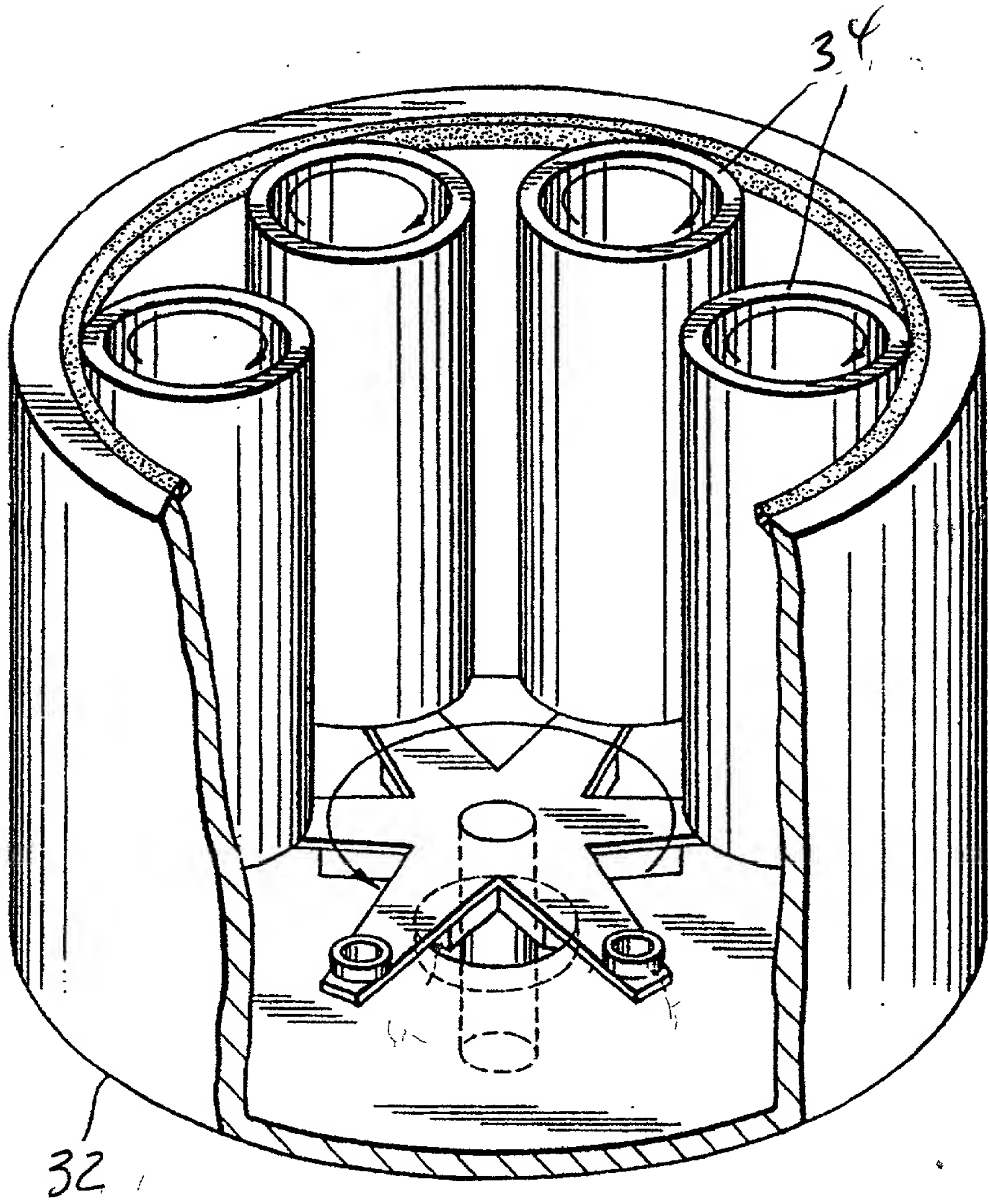


FIG. 4

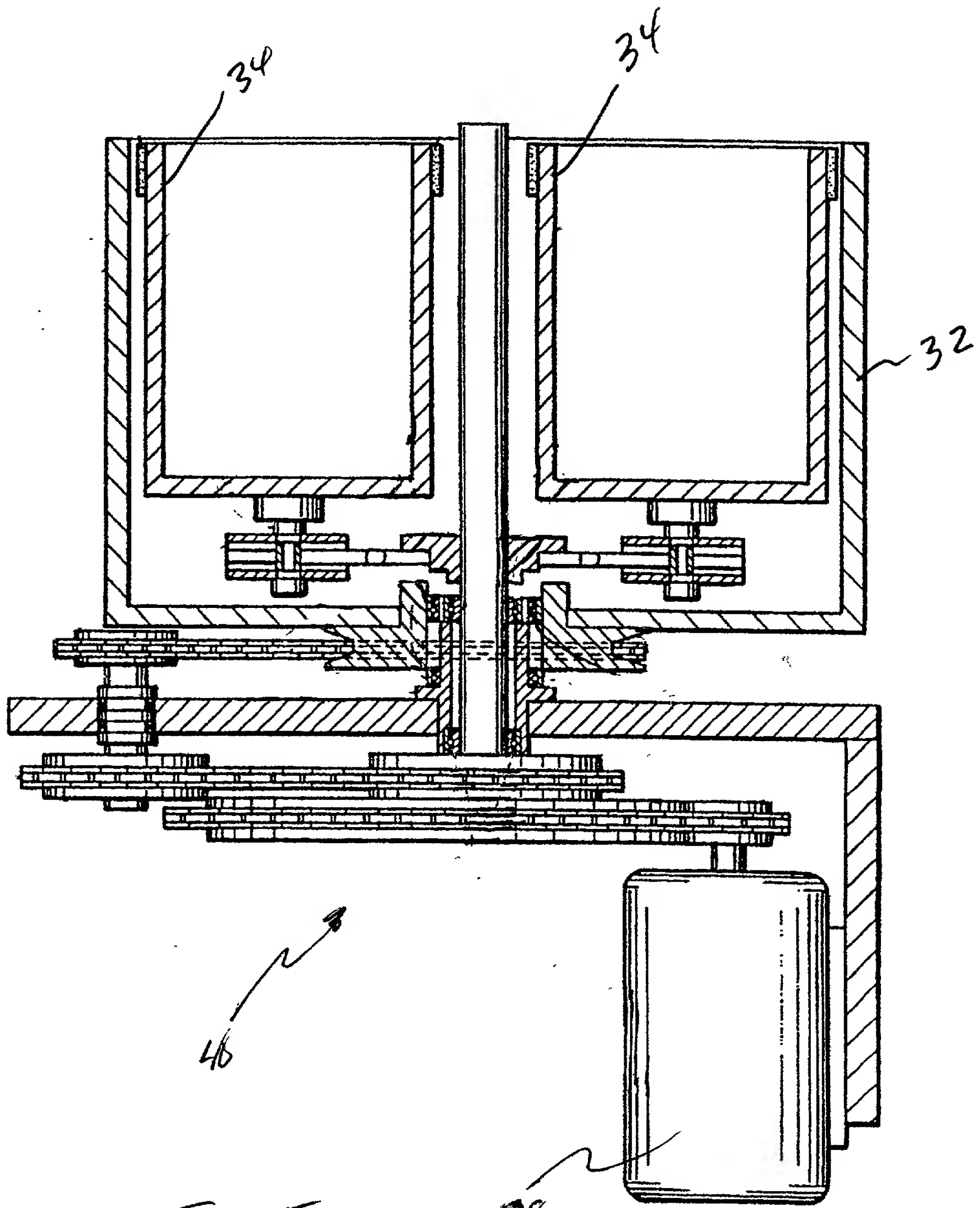


FIG. 5

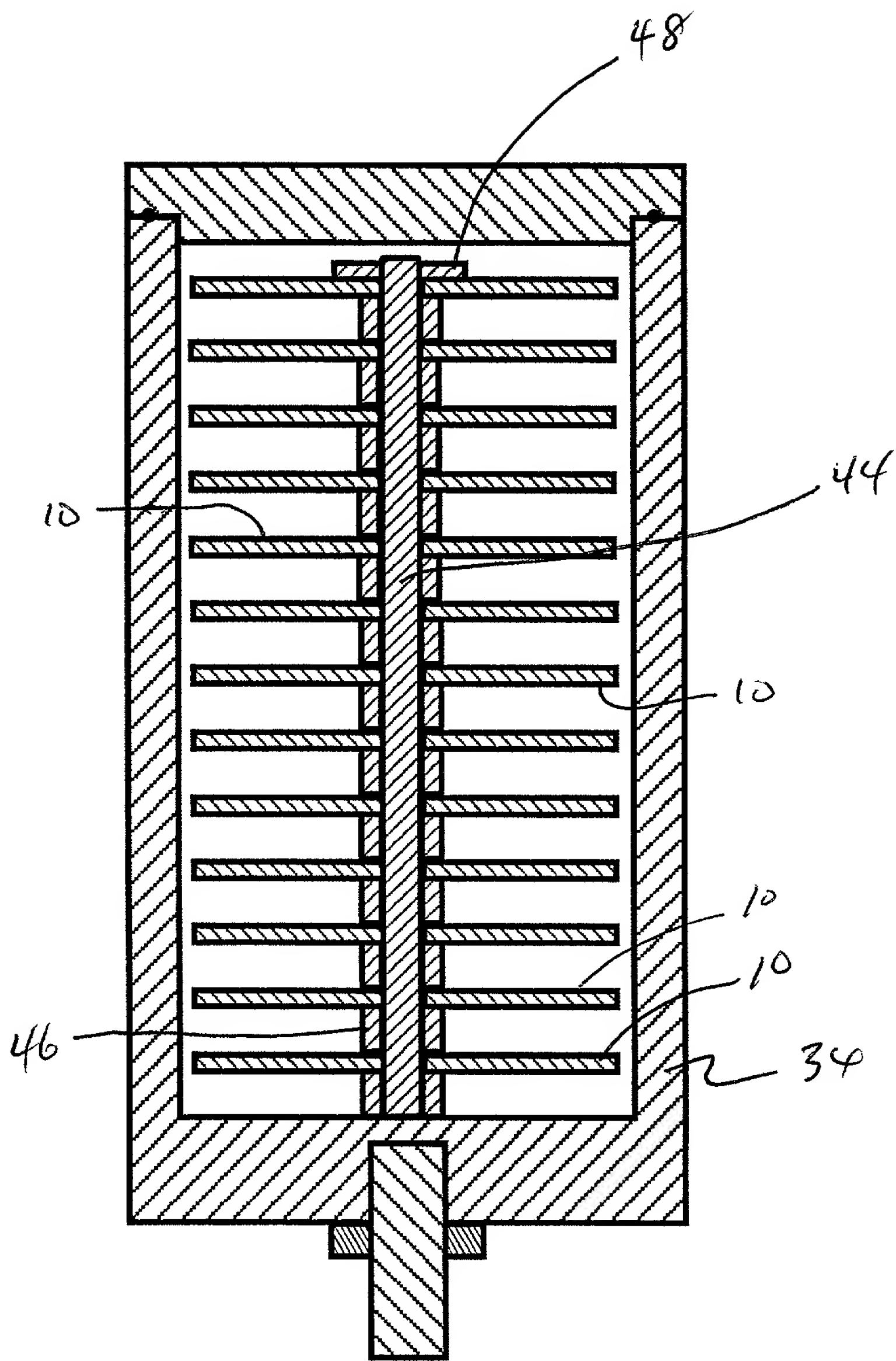


FIG. 6

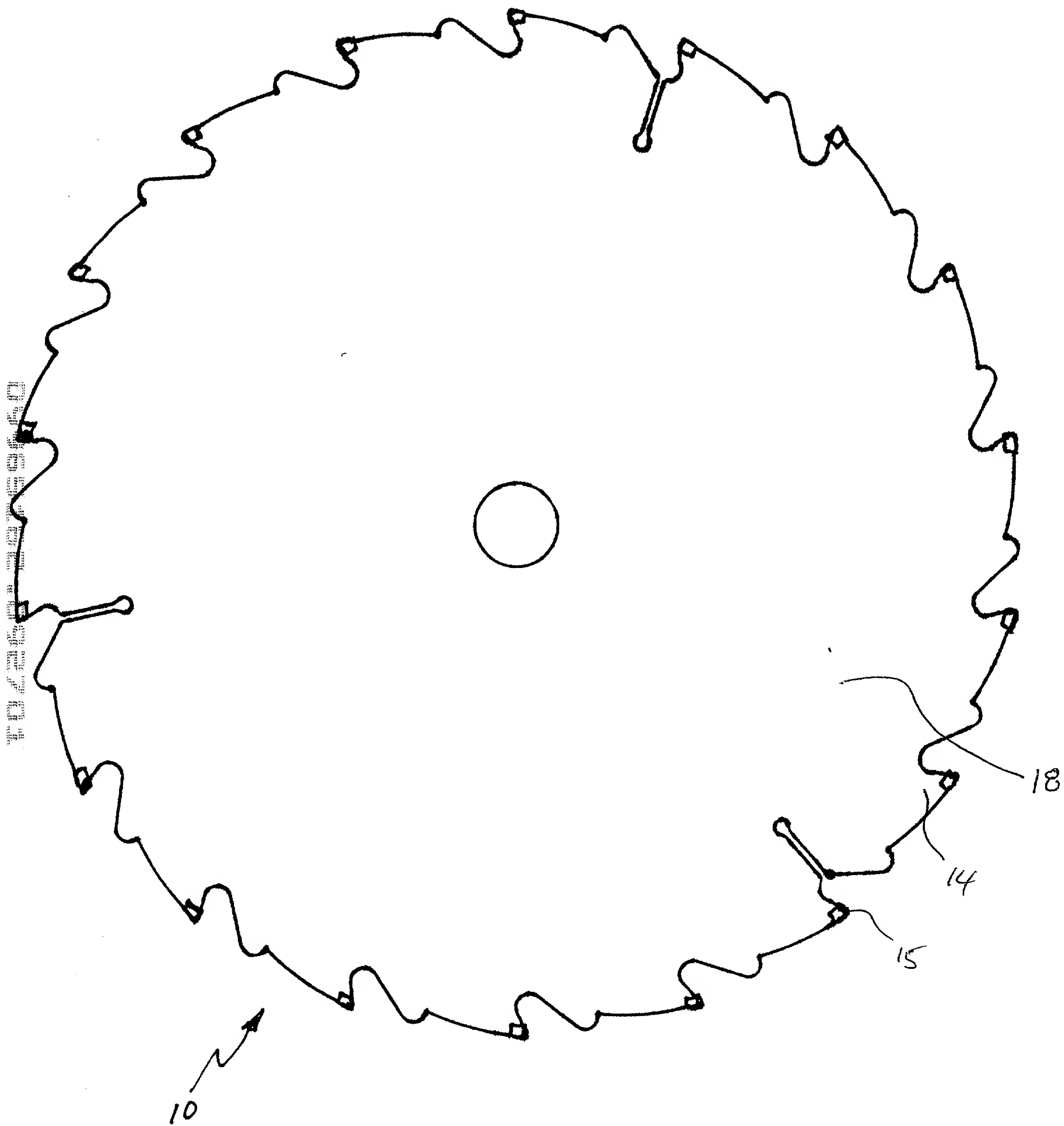


Fig. 7